



Sheet 1 of 1

FORM PTO-1449 (REV. 6-89)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office		Attorney's Docket No: 22178-05012.		Serial No. 09/872,435	
INFORMATION DISCLOSURE CITATION  (Use several sheets if necessary)				Applicant Stephen L. Bade, et al.			
				Filing Date June 1, 2001		Group Art Unit 2123	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
CVL	A	6052524	04/18/00	Pauna	395	500.43	
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
CVL	B	Berry, Gérard, <i>An Implementation of Constructive Synchronous Programs in POLIS</i> (Nov. 2, 1998), pp. 1-22.					
CVL	C	Lavagno, Luciano, <i>Models of Computation for Embedded System Design</i> ; (Sept. 28, 1998), pp. 1-57.					
EXAMINER		DATE CONSIDERED		12/19/05			
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PTO-1449  
REV: 02/01



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PTO/SB/08A (10-01)  
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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application No.	09/872,435
				Filing Date	June 1, 2001
				First Named Inventor	Stephen L. Bade
				Art Unit	2123
				Examiner Name	Not yet known
Sheet	1	of	1	Attorney Docket Number	22178-05012

U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Document No. Number - Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		US-		
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FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
CVL	A	EP 0 772 140 A	05/07/1997	Karl Van Rompaey et al.
CVL	B	EP 0867 820 A2	09/30/1998	Patrick Schaumont et al.

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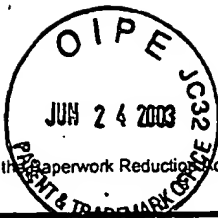
OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>6</sup>	
CVL	C	"Co-Verification Tools: A Market Focus"; Daya Nadamuni; Embedded Systems, Programming; Sept. 1999; Miller Freeman, USA; Vol. 12, No. 9; XP-001115094; ISSN: 1040-3272; Page 119-122.		
CVL	D	"Target Processor And Co-Verification Environment Independent Adapter-A Technology To Shorten Cycle-Time For Relargeting TI Processor Simulators In HW/SW Co-Verification Environments"; Rajesh Shah, Ramesh SubbaRa; Software Development Systems, Texas Instruments, India; ASIC/SOC Conference 1999. Proceedings Twelfth Annual IEEE; International Washington, DC USA Sept. 15-18, 1999, Piscataway, NJ, USA, IEEE, US; Sept. 15 1999; (1999-09-15); XP010360279A; ISBN: 0-7803-5632-2; pages 37-41.		
CVL	E	"A Top-Down Hardware/Software Co-Simulation Method For Embedded Systems Based Upon A Component Logical Bus Architecture"; Mitsuhiro Yasuda, Katsuhiko Seo, Hisao Koizumi, Mitsubishi Electric Corporation, Yokohama 220-81 Japan; Barry Shackelford, Hewlett-Packard Laboratories, Palo Alto, California 94304; Fumio Suzuki, Mitsubishi Electric Corporation, Amagasaki 661 Japan; Design Automation Conference. Proceedings of the ASP-DAC Asia And South Pacific; February 10, 1998 (1998-02-10); IEEE; XP002169112A; pages 169-175.		
CVL	F	"Hardware/Software Co-Verification, An IP Vendors Viewpoint"; Tim Hopes, EDA Engineering Manager, ARM Ltd.; Computer Design: VLSI In Computers And Processors, 1998. ICCD '98. Proceedings. International Conference on Austin, TX, USA 5-7 Oct. 1998, Los Alamitos, California, USA, IEEE Comput. Soc., U.S.; October 5, 1998 (1998-10-05), XP010310269A; ISBN: 0-8186-9099-2; pages 242-246		

Examiner Signature		Date Considered	12/19/05
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22178/05012/DOCS/1311284.1



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CVL		US-5,870,588	02-09-1999	Rompaey et al.

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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup>

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS			
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